

Docket No. 5000-5296

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No : Not Yet Assigned
Applicants : Takefumi NISHIMUTA, Hiroshi MIYAGI, Tadahiro OHMI, Shigetoshi
SUGAWA, and Akinobu TERAMOTO
Filed : Herewith
Title : DC AMPLIFIER AND SEMICONDUCTOR INTEGRATED CIRCUIT
THEREFOR
Art Unit : Not Yet Assigned
Examiner : Not Yet Assigned

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Commissioner for Patents
PO Box 1450
Alexandria, Va 22313 1450

PRELIMINARY AMENDMENT

Sir:

Applicants respectfully request that prior to examination, the claims of the
above-identified application be amended as shown hereafter.

Amendments to the Claims are reflected in the listing of claims which begins
on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.